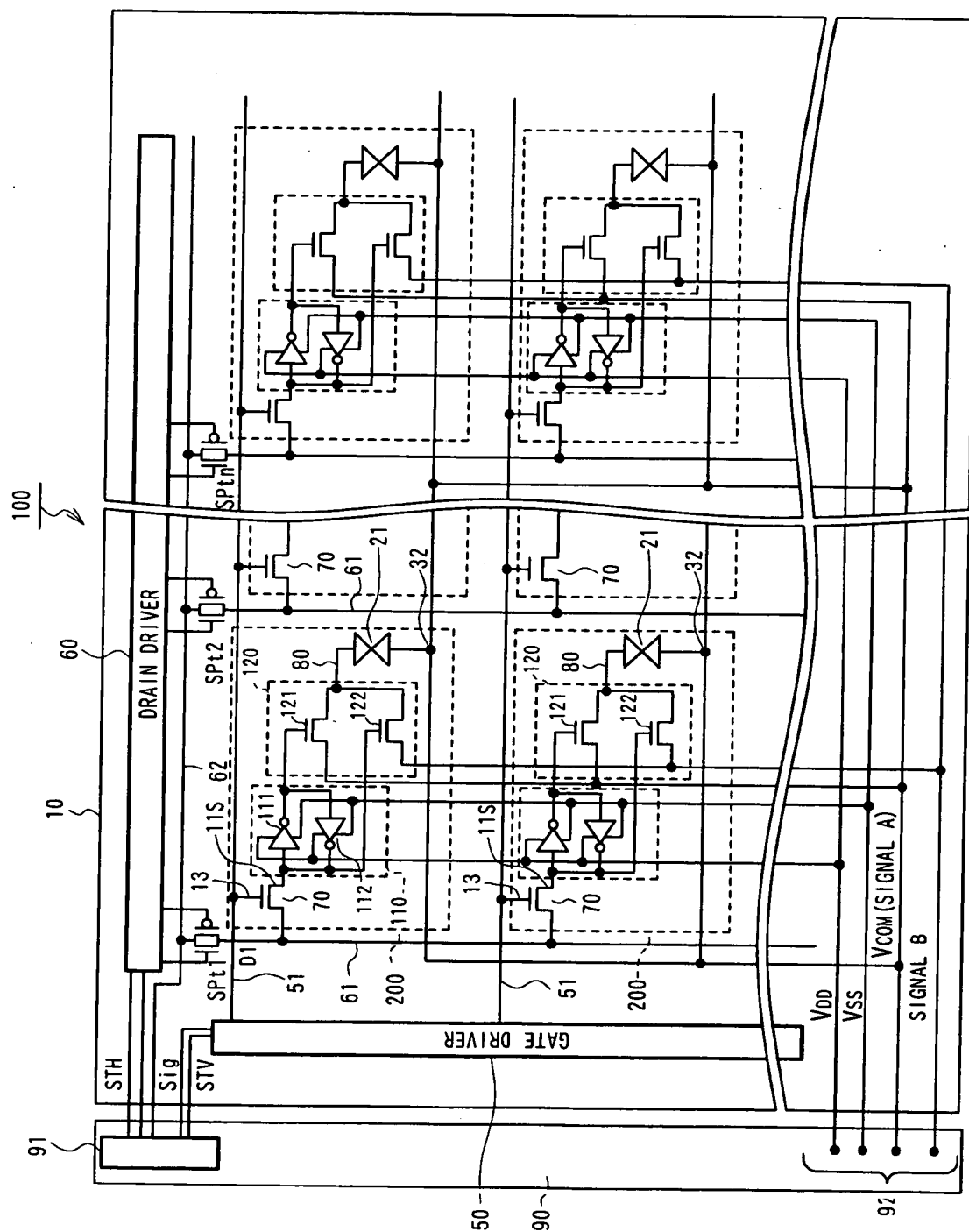


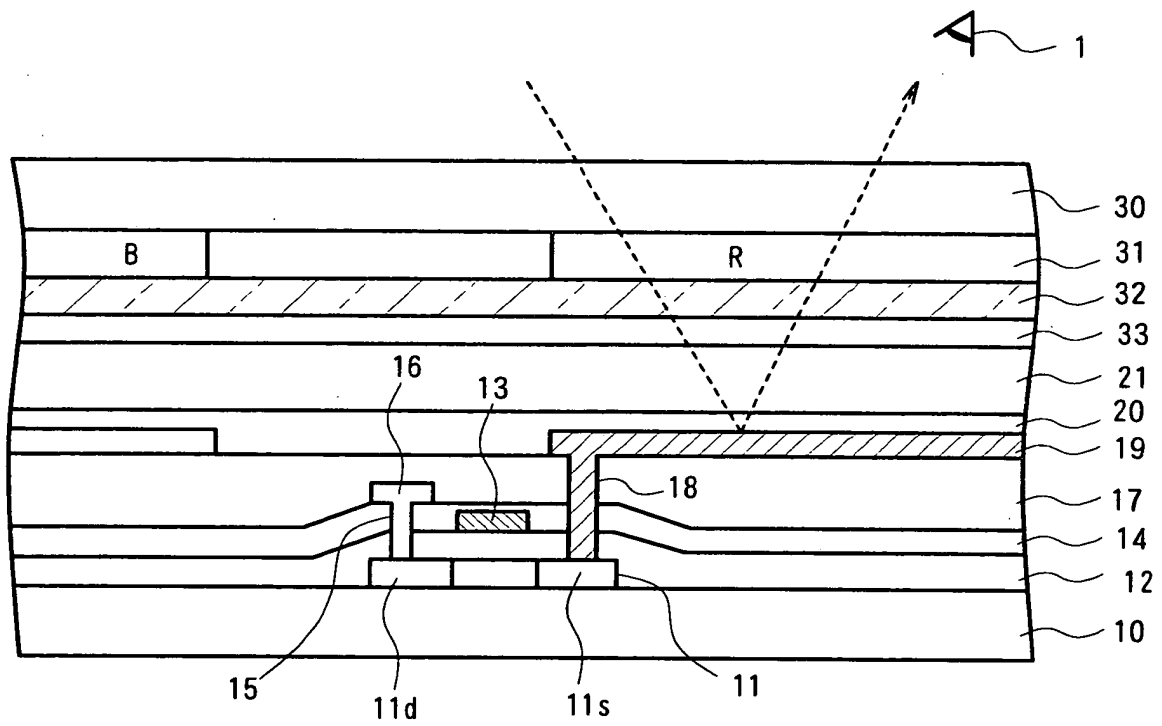
**Fig.1 PRIOR ART**



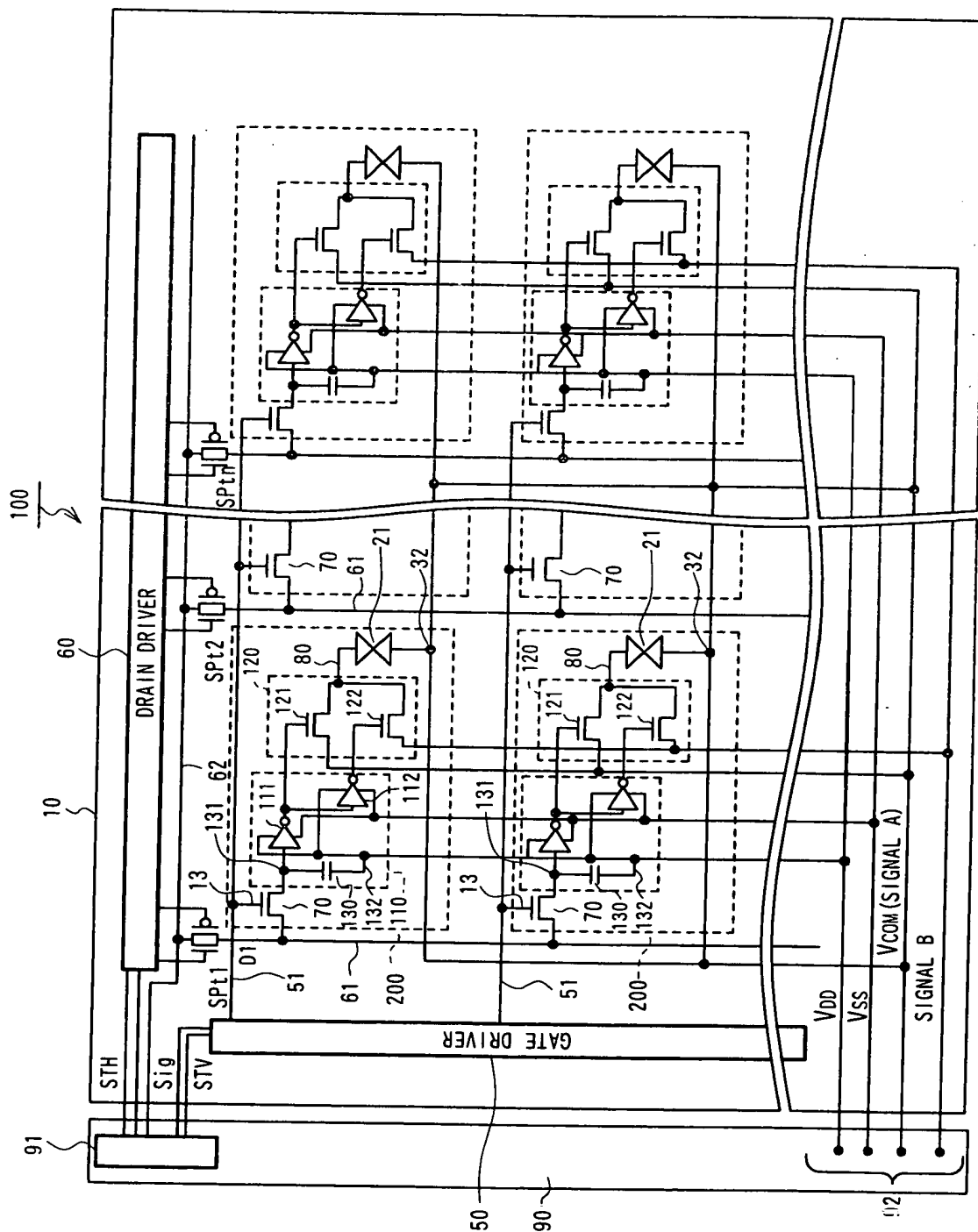
**Fig. 2A**



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**Fig.3**



**Fig. 4**

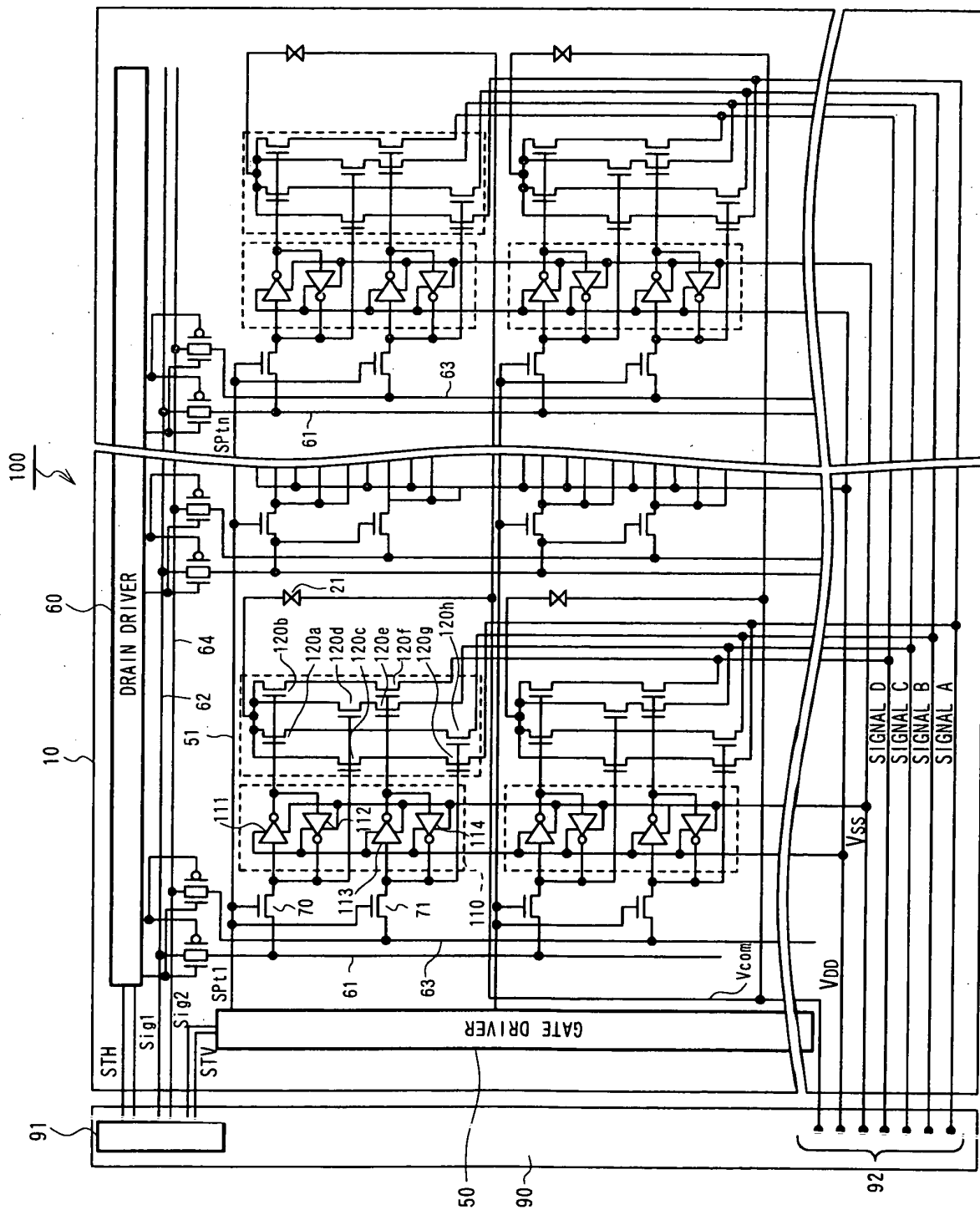


Fig. 5



The diagram shows a digital-to-analog converter (D/A) circuit. An input signal  $D_{in}$  passes through a switch 'n' to a D/A converter block labeled 130. The output of the D/A converter is connected to an output buffer labeled 131. The output of the buffer is connected to a switch SW1, which is controlled by a signal MD. The switch SW1 has two inputs: P1, which is connected to the MSB (Most Significant Bit) of the D/A converter, and P2, which is connected to the output of the output buffer 131. The output of the switch SW1 is labeled 'Sig TO LINE 62'.

**Fig. 7**



**Fig. 8**

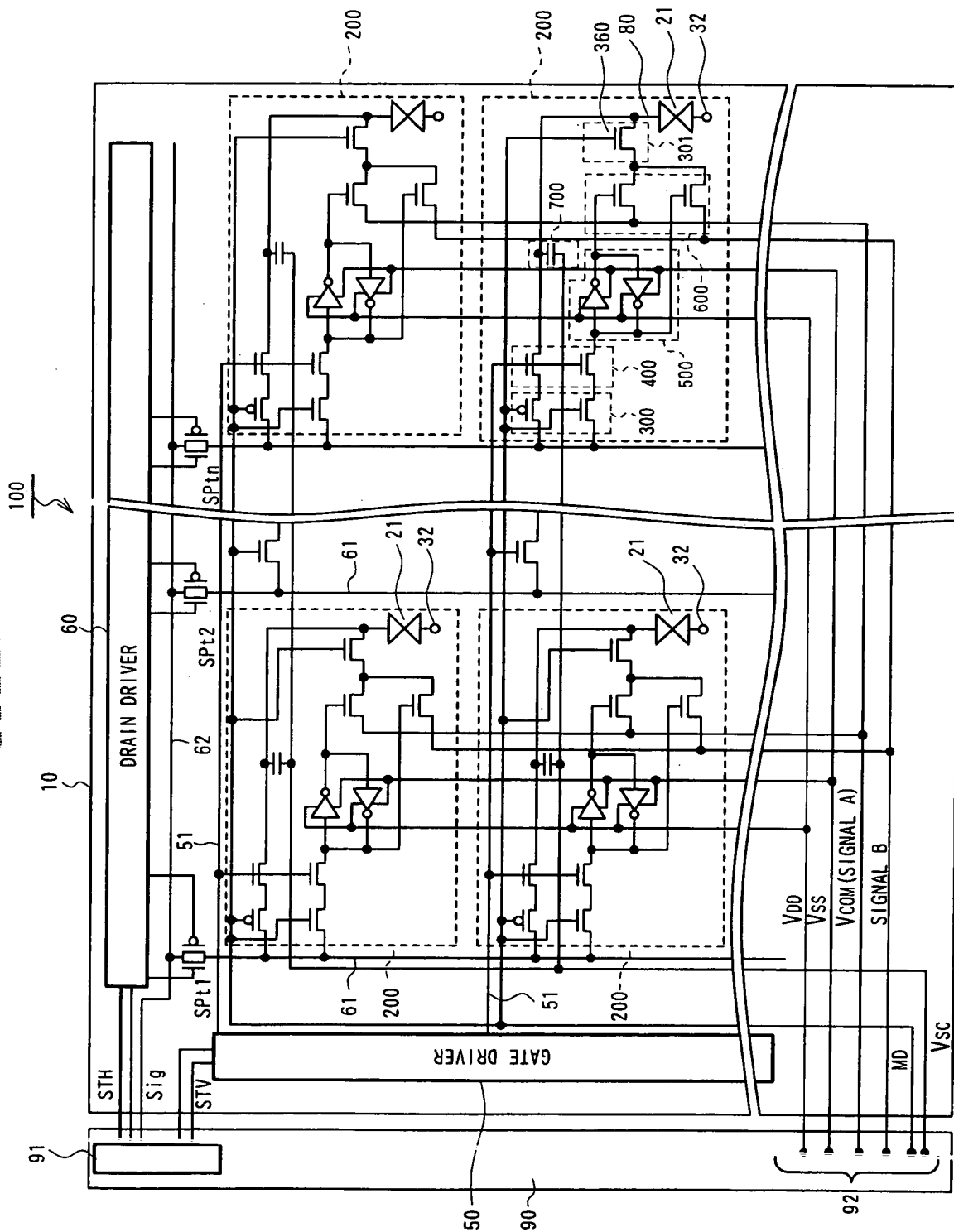


Fig. 9

